

Diagrams:

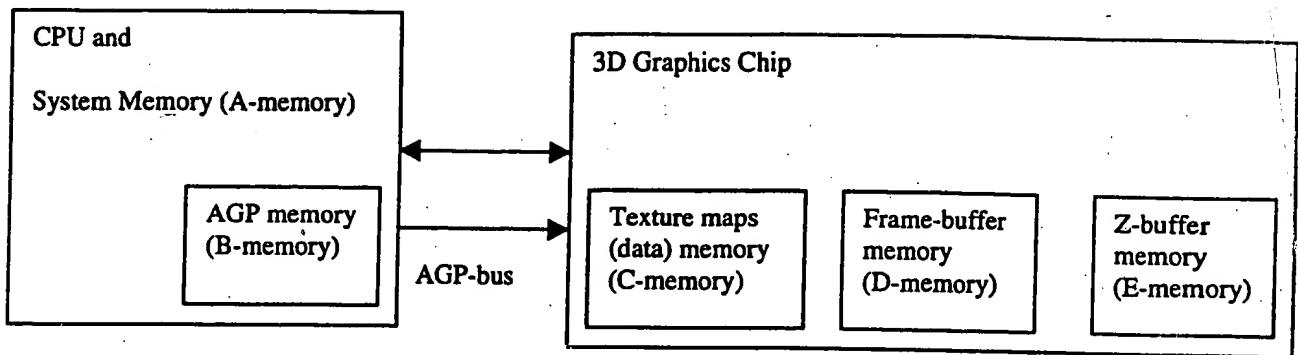


Fig. 1 Memory types and allocation in 3D-enabled electronic devices

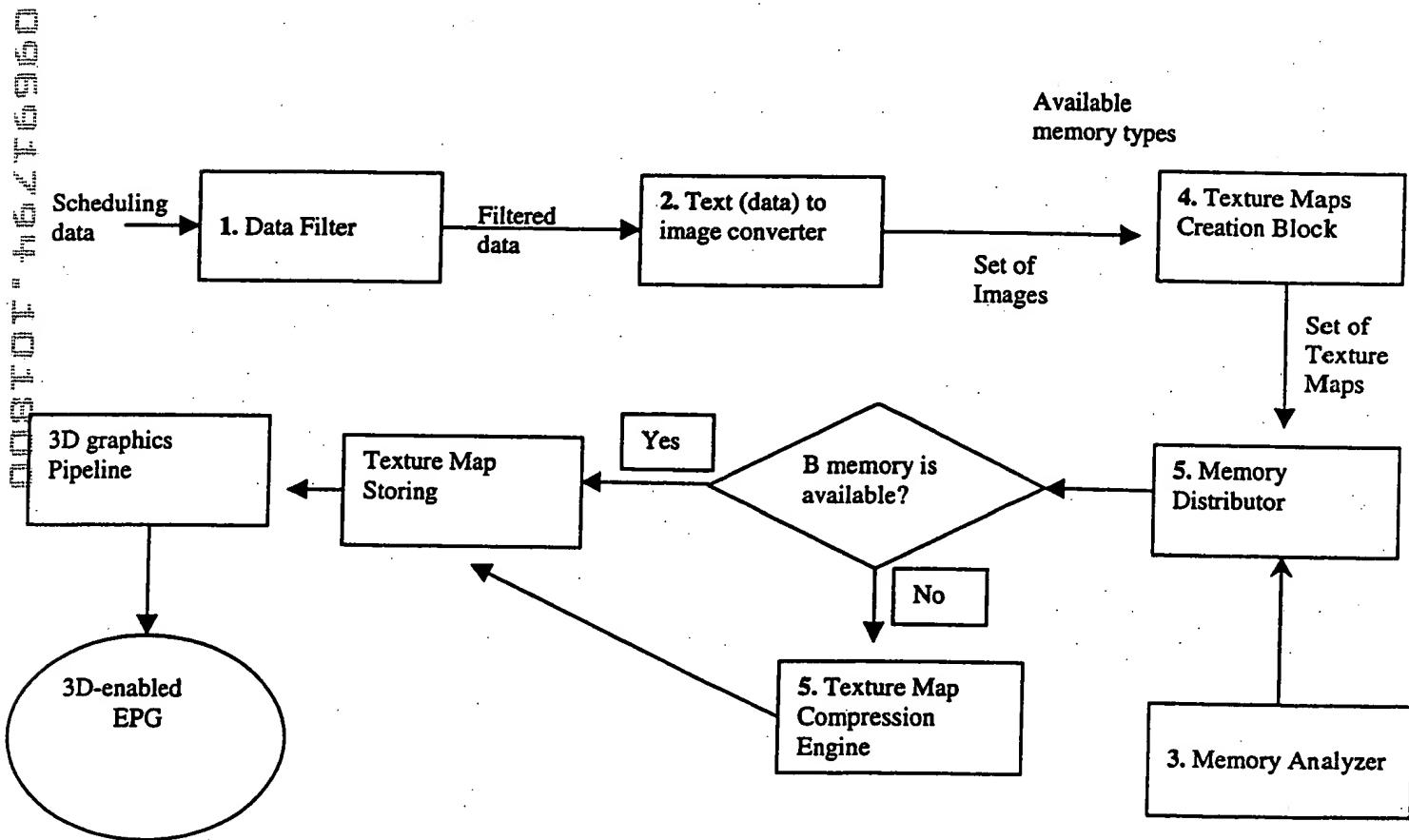


Fig. 2 Mechanism and Apparatus for cost-effective memory management diagram